OCT 3 0 2006 HE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Mahesh R. Junnarkar et al.

2883 Art Unit:

Serial No .:

10/669,206

Examiner:

Dinh D. Chiem

Filed:

September 24, 2003

Docket:

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ITL.0995US

P16440

For:

Temperature Tuned Arrayed

Waveguide Grating

Assignee:

Intel Corporation

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed August 25, 2006, please amend the abovereferenced patent application as follows:

Date of Deposit: October 26, 2006

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents,

VA 22313-1450

L. Hayden